



Please type a plus sign (+) inside this box → ☐

Modified 9-98

PTO/SB/21 (12-97)  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE  
Approved for use through 9/30/00. OMB 0651-0031

pbs

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

BOX:

Express Mail  
Receipt No.

Application / Conf. No.	10/090,239 6539
Filing Date	March 01, 2002
First Named Inventor	Atul V. Ghia
Examiner Name	Unknown
Group Art Unit	2819
Issue Fee Batch No.	
Attorney Docket Number	X-1061 US

## ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
<input checked="" type="checkbox"/> Amendment / Response	<input type="checkbox"/> Declaration / Oath	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input checked="" type="checkbox"/> Preliminary Amendment	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Affidavit(s)/declaration(s)	<input type="checkbox"/> Petition -	<input checked="" type="checkbox"/> Return Receipt Postcard
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> To Convert a Provisional Application	<input type="checkbox"/> Additional Enclosure(s) (please identify below):
<input type="checkbox"/> Change Status to LARGE ENTITY	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	
<input type="checkbox"/> Information Disclosure Statement (Supplemental)	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A)		
<input type="checkbox"/> Certified Copy of Priority Document(s)		
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

Remarks

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	24309	Reg. Number	40,941
Attn:	Lois D. Cartier		
Signature			
Date	November 12, 2002	Charge any additional fees required/credit any overpayment to our Deposit Account Number:	24-0040

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231 on this date: November 12, 2002

Typed or Printed Name	Pat Slaback	Date	November 12, 2002
Signature			

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Approved for use through 10-31-2002. OMB 0651-0032  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

# FEE TRANSMITTAL for FY 2003

Patent fees are subject to annual revision

TOTAL AMOUNT OF PAYMENT (\$ 426.00

## Complete if Known

Application / Conf. No.	10/090239
Filing Date	March 1, 2002
First Named Inventor	Atul V. Ghia
Examiner Name	Unknown
Group / Art Unit	2819
Attorney Docket No.	X-1061 US

## METHOD OF PAYMENT (check one)

1. ☒ The Commissioner is hereby authorized to charge indicated fees, any additional fees required, and credit any over payments to:

Deposit  
Account  
Number

24-0040

Deposit  
Account  
Name

XILINX, INC.

☐ Charge the any additional Fee Required  
Under 37 CFR §§ 1.16 and 1.17

2. ☐ Payment Enclosed:  
☐ Check ☐ Money Order ☐ Other

## FEE CALCULATION

### 1. BASIC FILING FEE

#### Large Entity

Fee Code	Fee (\$)	Fee Description	Fee
101	740	Utility filing fee	
106	330	Design filing fee	
107	540	Plant filing fee	
108	710	Reissue filing fee	
114	150	Provisional filing fee	

SUBTOTAL (1) (\$)

### 2. EXTRA CLAIM FEES

Total Claims	-20**	X	Extra	Fee from below	Fee Paid
Indep. Claims	05	-3** =	19	18	\$342
Multiple Dependent Claims			1	84	\$84

\*\*or number previously paid, if greater; For Reissues, see below

Large Entity	Fee Code	Fee (\$)	Fee Description
	103	18	Claims in excess of 20
	102	84	Independent claims in excess of 3
	104	270	Multiple dependent claim, if not paid
	109	84	**Reissue independent claims over original patent
	110	18	**Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$ 426.00

## FEE CALCULATION (continued)

### 3. ADDITIONAL FEES

Large Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
105	130	Surcharge - late filing fee or oath	
127	50	Surcharge - late provisional filing fee or cover sheet.	
147	2,520	For filing a request for exparte reexamination	
112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	Requesting publication of SIR after Examiner action	
115	110	Extension for reply within first month	
116	400	Extension for reply within second month	
117	920	Extension for reply within third month	
118	1,440	Extension for reply within fourth month	
128	1,960	Extension for reply within fifth month	
119	320	Notice of Appeal	
120	320	Filing a brief in support of an appeal	
121	270	Request for oral hearing	
138	1,510	Petition to institute a public use proceeding	
140	110	Petition to revive - unavoidable	
141	1,240	Petition to revive - unintentional	
142	1,280	Utility issue fee (or reissue)	
122	130	Petitions to the Commissioner	
123	50	Petitions related to provisional applications	
126	180	Submission of Information Disclosure Stmt	
581	40	Recording each patent assignment per property (times number of properties)	
146	740	Filing a submission after final rejection (37 CFR 1.129(a))	
149	740	For each additional invention to be examined (37 CFR 1.129(b))	
179	740	Request for Continued Examination (RCE)	

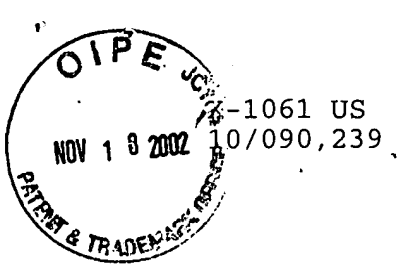
Other fee (specify)

\*Reduced by Basic Filing Fee Paid SUBTOTAL (3) (\$)

## SUBMITTED BY

Name (Print/Type)	Lois D. Cartier	Registration No. (Attorney/Agent)	40,941	Telephone	720-652-3733
Signature		Date	11-12-2002		

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.



PATENT  
Conf. No. 6539

IN THE UNITED STATES PATENT OFFICE

Applicants: Atul V. Ghia et al.  
Assignee: Xilinx, Inc.  
Title: Low Jitter Clock for a Physical Media Access  
Sublayer on a Field Programmable Gate Array  
Serial No.: 10/090,239 Filing Date: 3/1/2002  
Examiner: Unknown Art Unit: 2819  
Docket No.: X-1061 US Conf. No.: 6539

-----  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

PRELIMINARY AMENDMENT

Before taking action in the above-referenced case, please  
make the following amendments to the claims.

IN THE CLAIMS

**Please replace Claim 41 with the following:**

41. (Amended) The programmable logic device of Claim 40,  
further comprising means for converting the PMA reference clock  
signal to a lower voltage signal for use in a physical coding  
sublayer of the multi-gigabit transceiver.

**Please add Claims 42-60 as follows:**

11/20/2002 DTESSEN1 00000012 240040 10090239

01 FC:1202 342.00 CH  
02 FC:1201 84.00 CH

42. (New) A system, comprising:

a programmable logic device including:

programmable input/output circuitry,

programmable core logic coupled to the

programmable input/output circuitry,

a multi-gigabit transceiver coupled to the

programmable core logic,

a first pair of clock pads, and

a dedicated routing structure directly connecting  
the first pair of clock pads and the multi-gigabit  
transceiver; and

a clock generation circuit coupled to the first pair of  
clock pads.